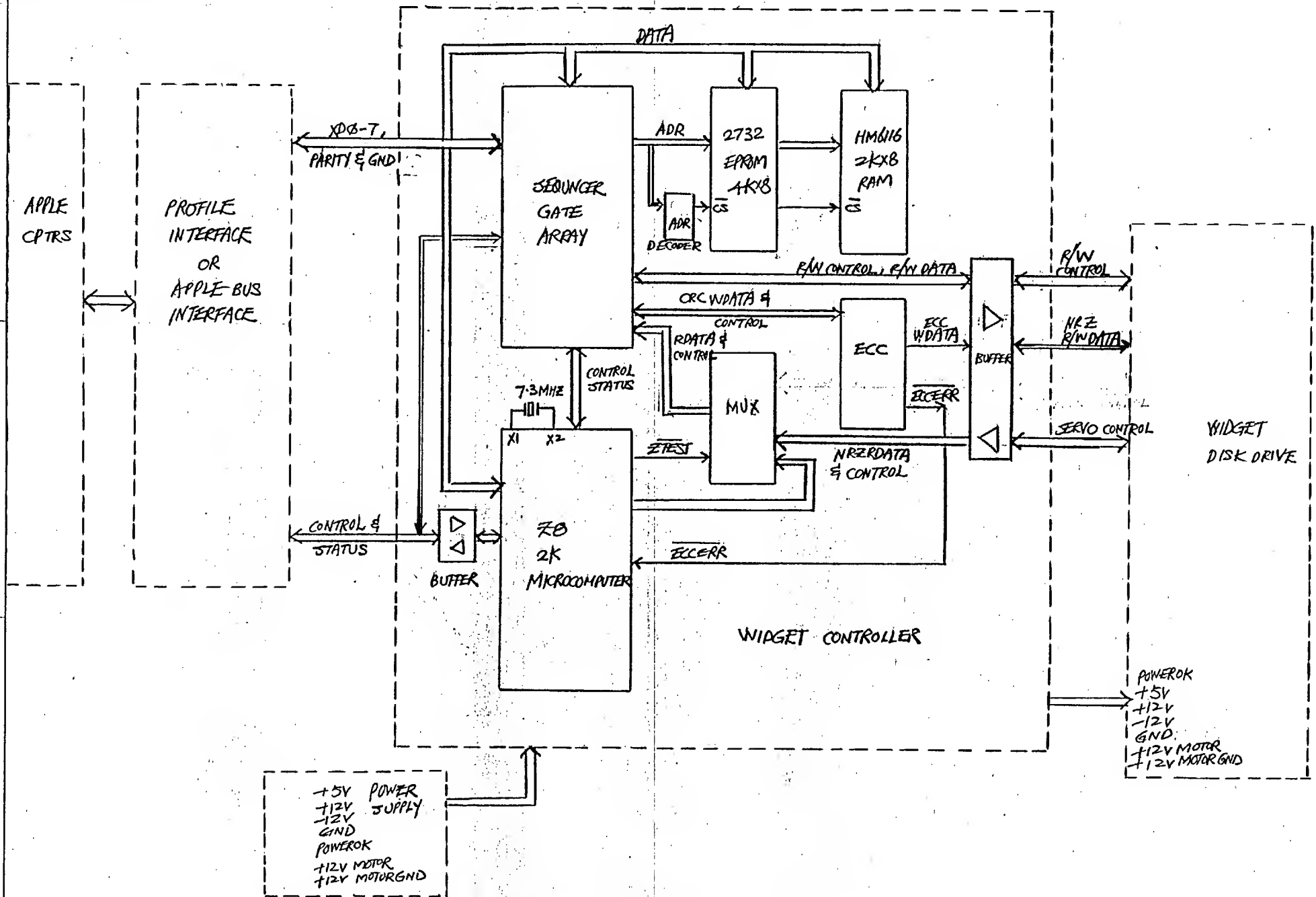


# WIDGET CONTROLLER BLOCK DIAGRAM

P8



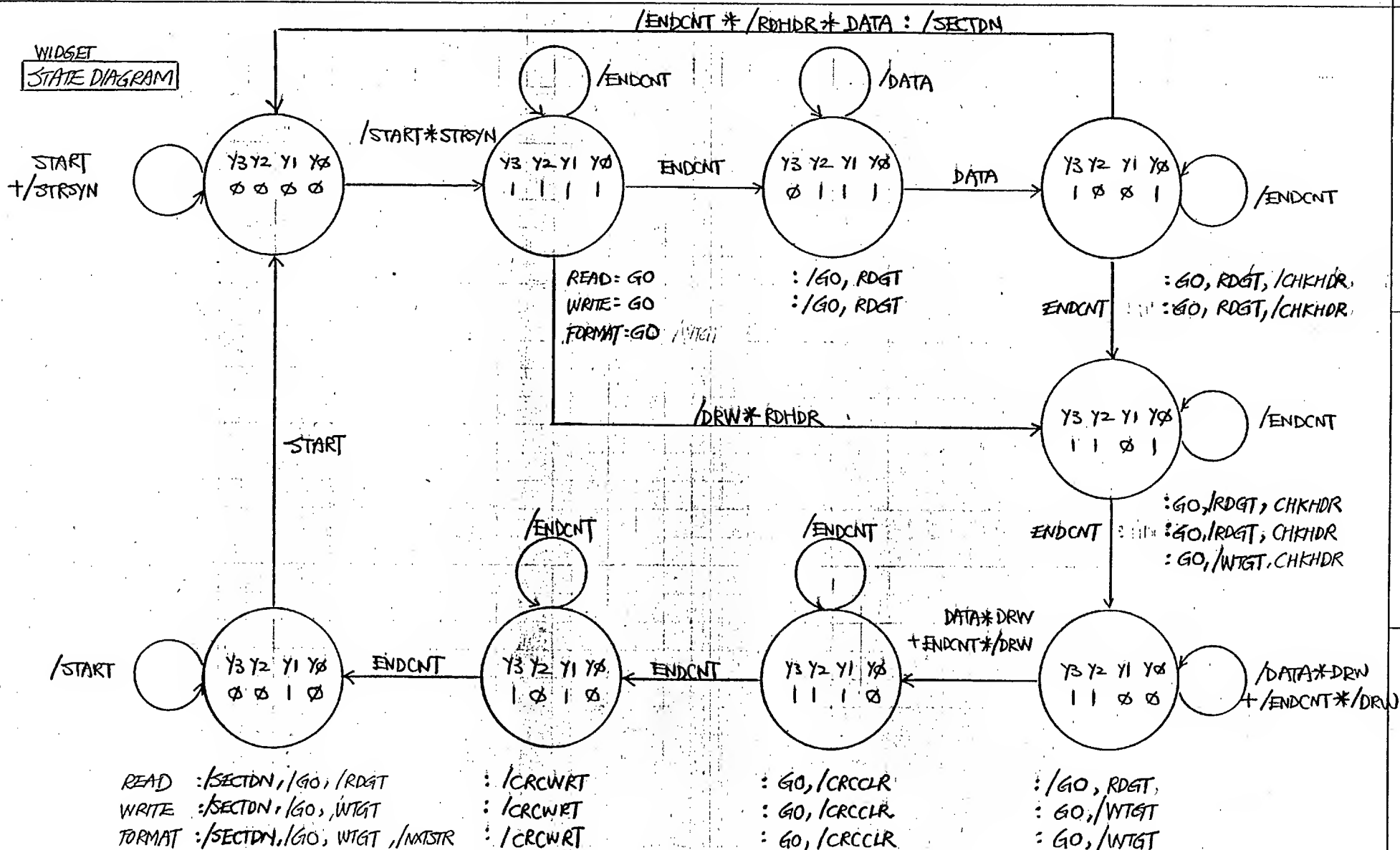
10/18/82



42-381 50 SHEETS 5 SQUARE  
42-382 100 SHEETS 5 SQUARE  
42-389 200 SHEETS 5 SQUARE

P1

WIDSET  
STATE DIAGRAM



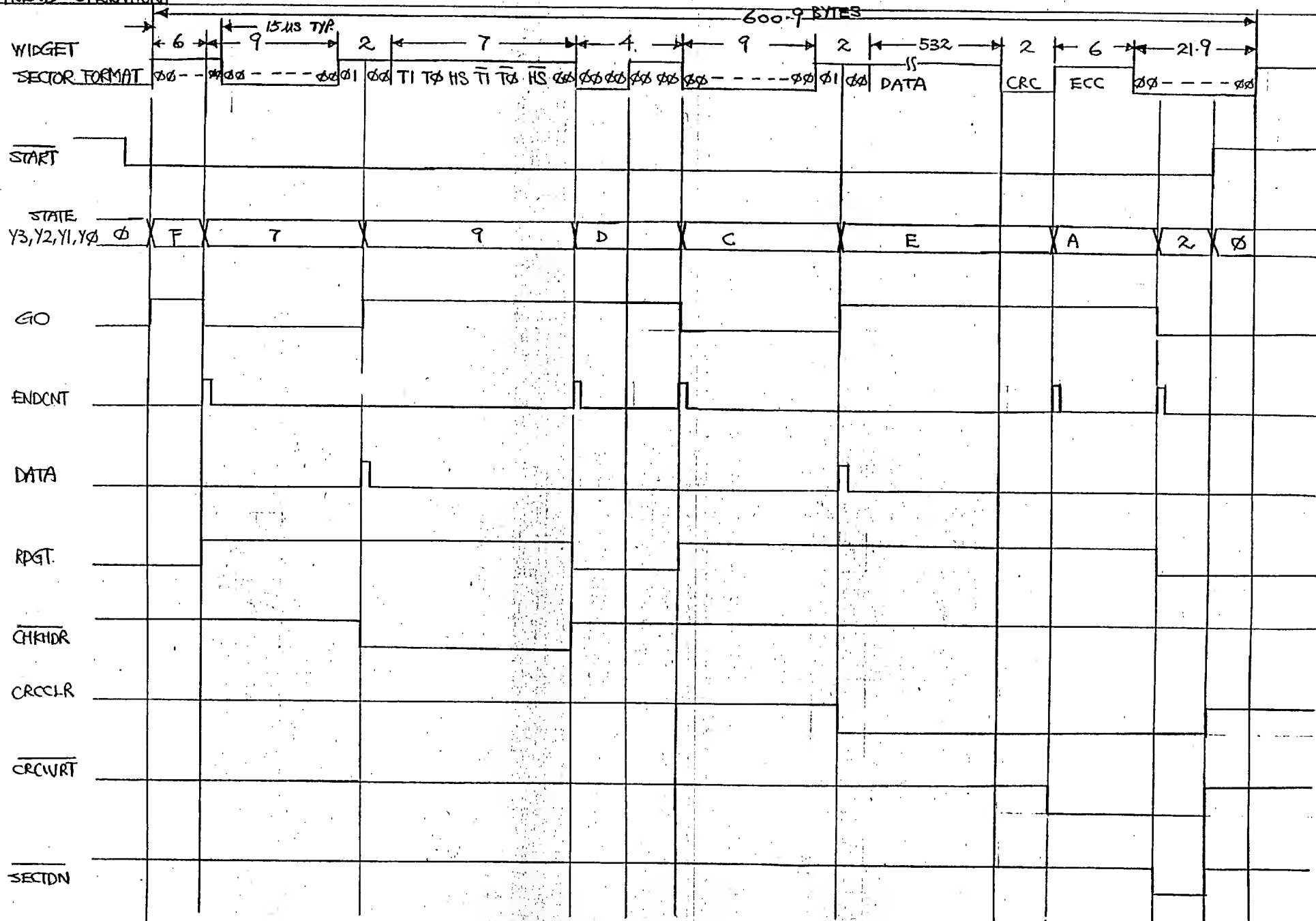
10/18/82

READ OPERATION



42-381 50 SHEETS 5 SQUARE  
42-382 100 SHEETS 5 SQUARE  
42-383 200 SHEETS 5 SQUARE

P2A



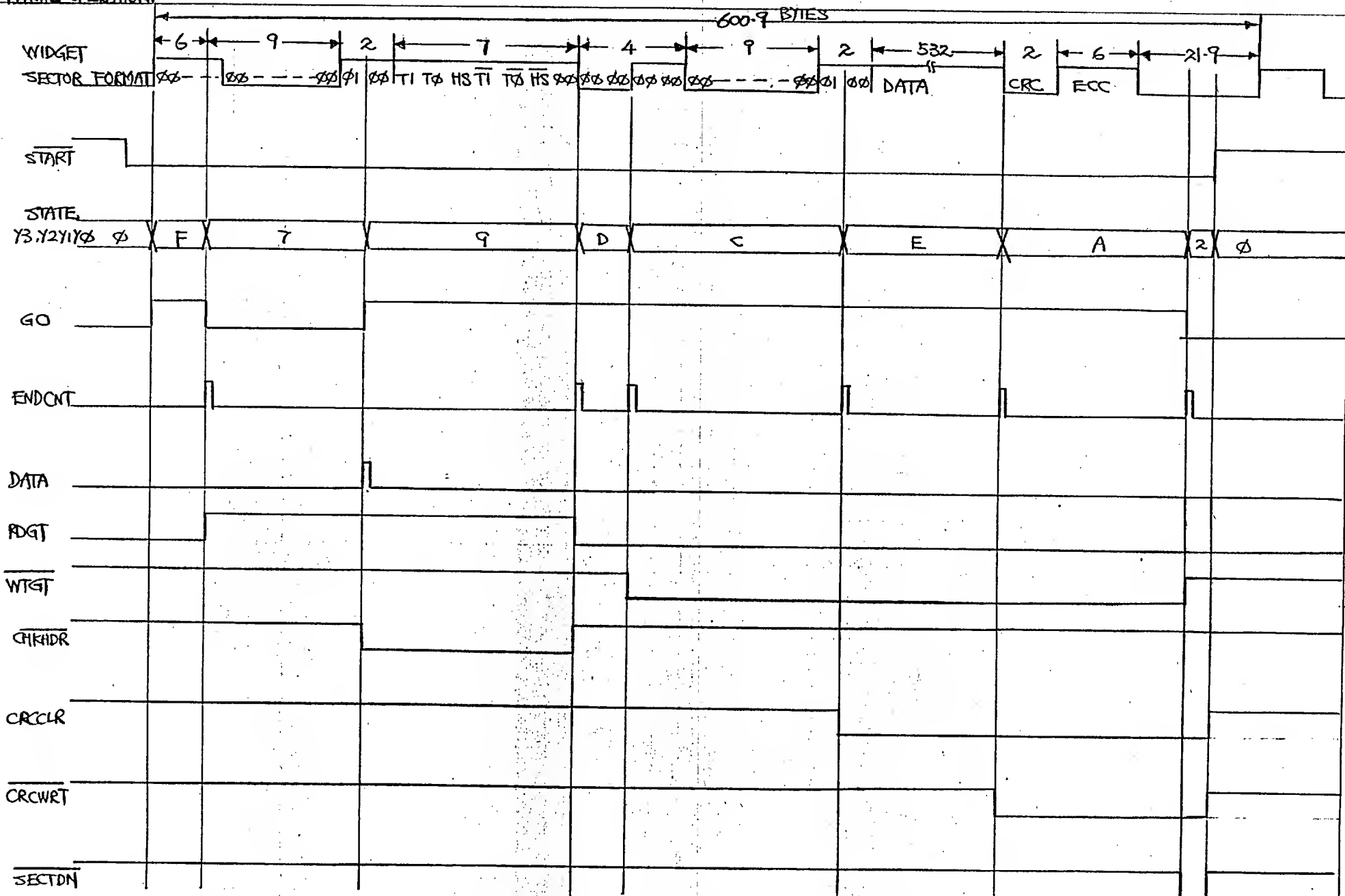
10/18/82



42-381 50 SHEETS 3 SQUARE  
42-382 100 SHEETS 3 SQUARE  
42-389 200 SHEETS 3 SQUARE  
MADE IN U.S.A.

# WRITE OPERATION

P3A



10/18/82

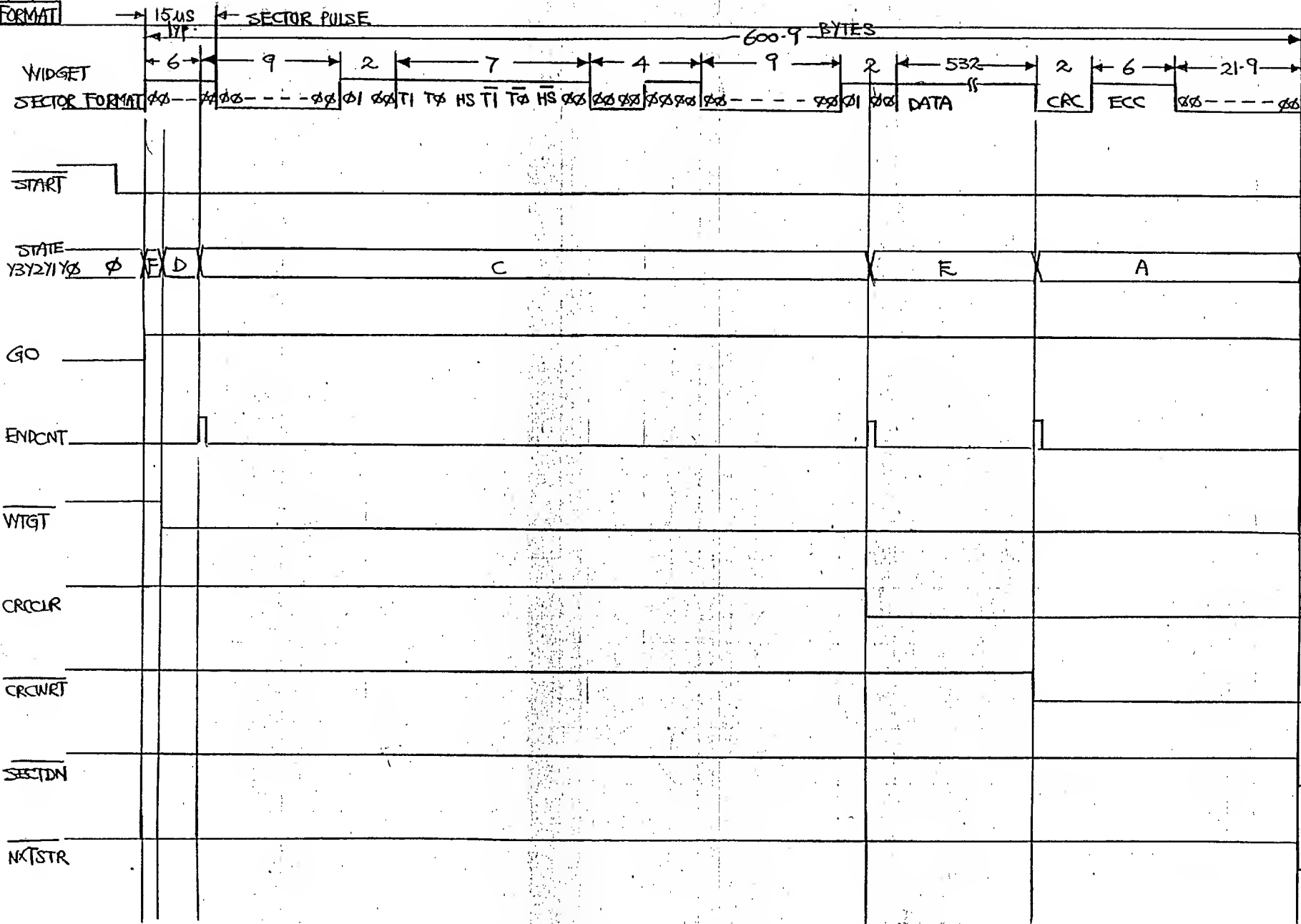
FORMAT



42-381 30 SHEETS 5 SQUARE  
42-382 100 SHEETS 5 SQUARE  
42-383 200 SHEETS 5 SQUARE

MADE IN U.S.A.

P4



10/18/82

WIDGET

TIMING

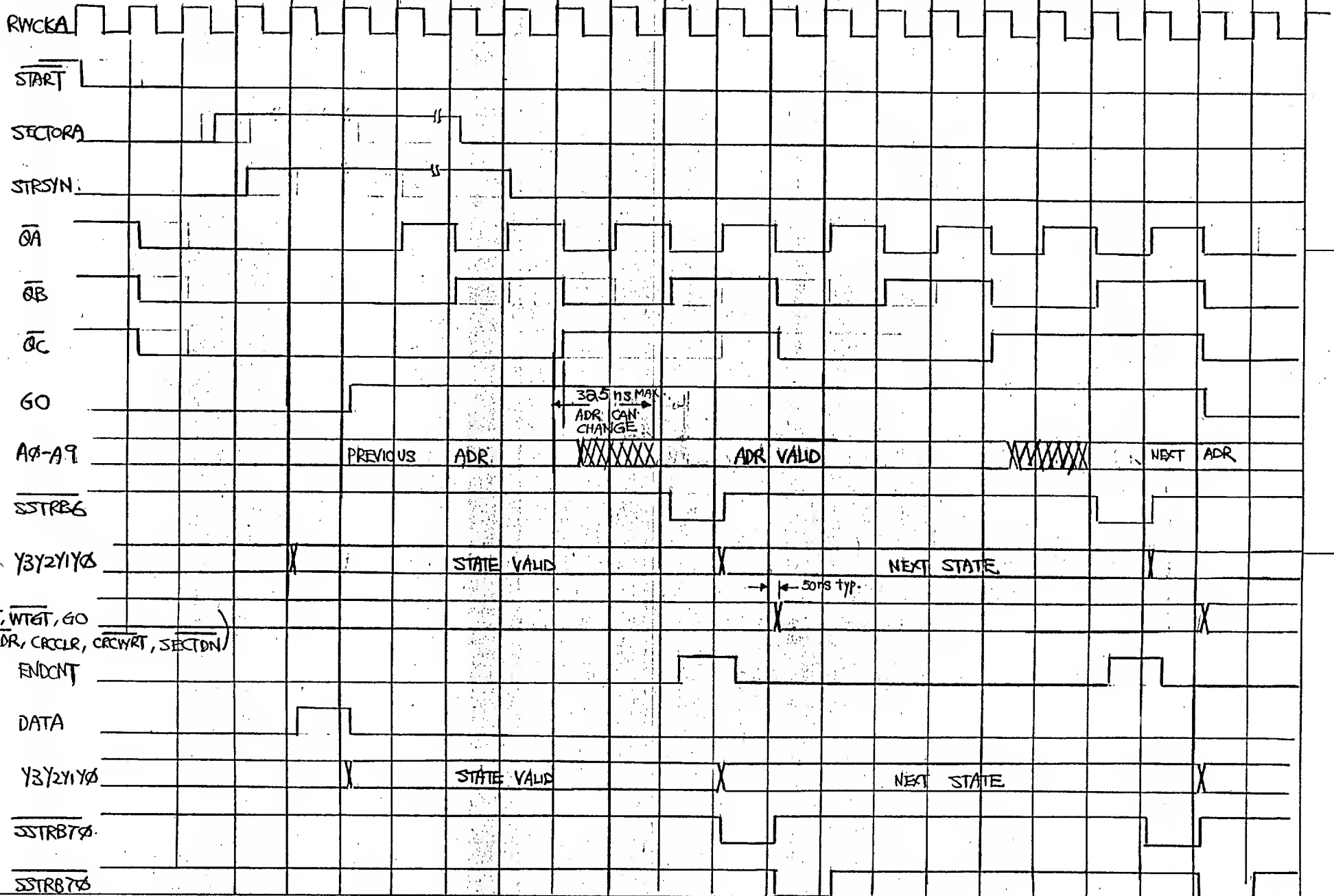


42-381 50 SHEETS 5 SQUARE  
42-382 100 SHEETS 5 SQUARE  
42-389 200 SHEETS 5 SQUARE  
MADE IN U.S.A.

P5

130ns  
200ns  
MIN  
TYP.

PH0 PH1 PH2 PH3 PH4 PH5 PH6 PH7 PH8 PH9 PH10 PH11 PH12 PH13 PH14 PH15 PH16 PH17 PH18

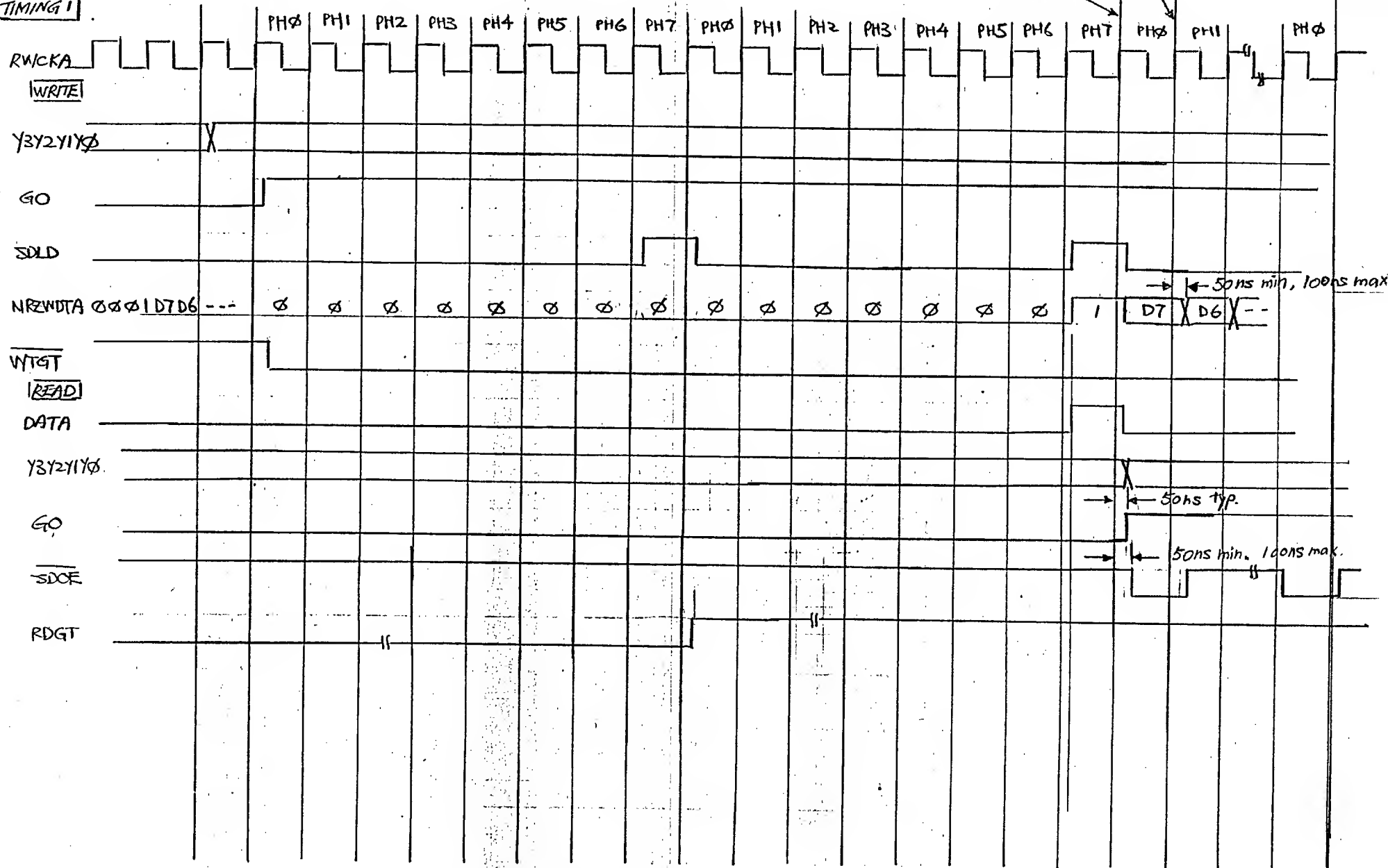


10/18/82



42-381 50 SHEETS 5 SQUARE  
42-382 100 SHEETS 5 SQUARE  
42-389 200 SHEETS 5 SQUARE  
Made in U.S.A.

**WIDGET  
TIMING 1**



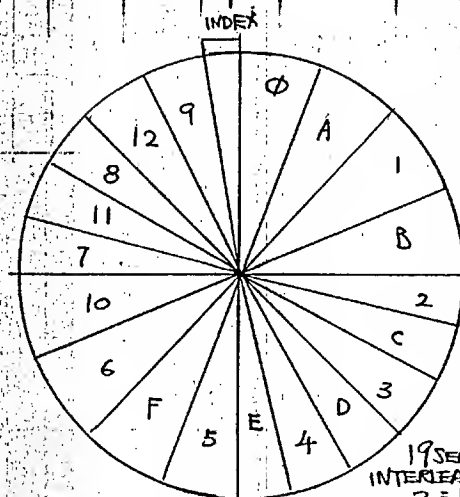
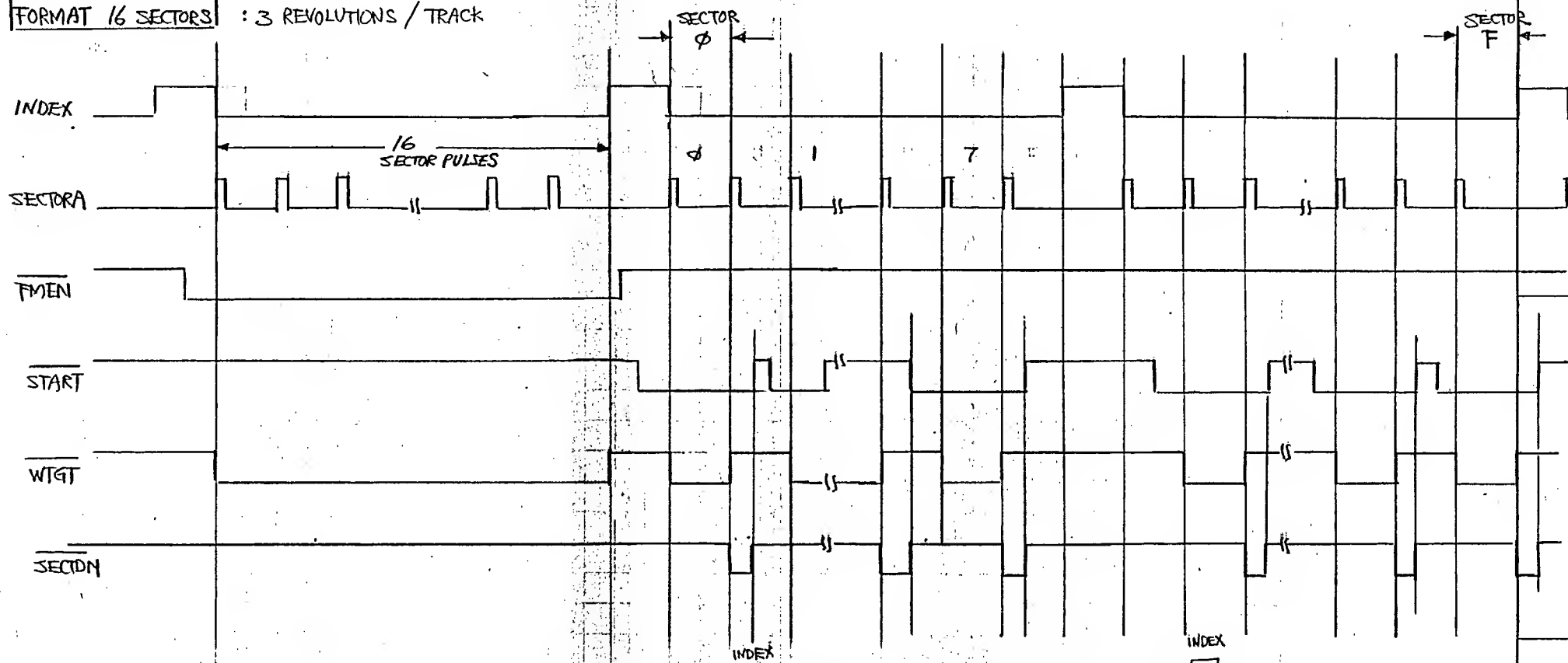


42-381	50 SHEETS	5 SQUARE
42-382	100 SHEETS	5 SQUARE
42-389	200 SHEETS	5 SQUARE

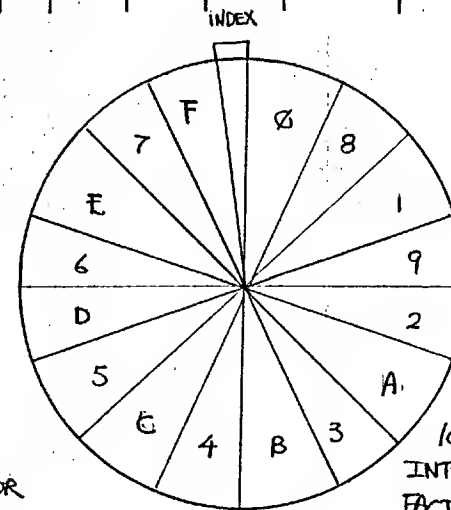
Made in U. S. A.

P7

**FORMAT 16 SECTORS : 3 REVOLUTIONS / TRACK**



19 SECTORS  
INTERLEAVING FACTOR  
 $3 = 1$



✓ 16 sectors.  
INTERLEAVING  
FACTOR 2 : 1



10/18/82

WIDGET

P8

	SI (SERVOIN) <del>ZRWCK</del> HSØ DM SECTION INDEX SECTOR SO (SERVOOUT)	DRW SIORDY MSEL1 MSELØ BUSY CMD SERVORST START	RDHDR TEST FMEN MBSØ A11/PC A1Ø/RWL AØ9 AØ8	AD7 / CRCERR AD6 / WRTVALID AD5 / SERVORDY AD4 / SERVORERR AD3 / Y3 AD2 / Y2 AD1 / Y1 ADØ / YØ	
FUNCTIONS	0 0 0 0 1 1 1 1 P37 P36 P35 P34 P33 P32 P31 P3Ø	0 1 0 0 0 1 0 0 P27 P26 P25 P24 P23 P22 P21 P2Ø	0 0 0 0 PØ7 PØ6 PØ5 PØ4	0 0 0 0 PØ3 PØ2 PØ1 PØØ	7/Ø 7/Ø 7/Ø 7/Ø 7/Ø 7/Ø 7/Ø 7/Ø P17 P16 P15 P14 P13 P12 P11 P1Ø
APPLE WR RAM	0	Ø Ø Ø	1 X 1 1	OUTPUT	HIZ
APPLE RD RAM	0	1 Ø Ø	1 X 1 1	OUTPUT	HIZ
APPLE RD/WR Z8	X	X 1 1	1 X 1 1	OUTPUT	I/O
SEQUNCER RD DISK DATA	0	1 1 Ø	Ø Ø 1 1	OUTPUT	HIZ
SEQUNCER WR DISK DATA	0	Ø 1 Ø	Ø Ø 1 1	OUTPUT	HIZ
SEQUNCER RD ID	0	1 1 Ø	Ø 1 1 1	OUTPUT	HIZ
SEQUNCER FORMAT	0	Ø 1 Ø	Ø 1 1 1	OUTPUT	HIZ
SEQUNCER WR ZF	1	X X X	1 X 1 Ø	ADR	ADR/DATA
Z8 LOAD RAM ADR	1	X 1 Ø	1 X 1 1	ADR	ADR/DATA
Z8 RD/WR RAM	ADR	X Ø 1	1 X 1 1	ADR	ADR/DATA

10/18/82

# MEMORY DATA FORMAT

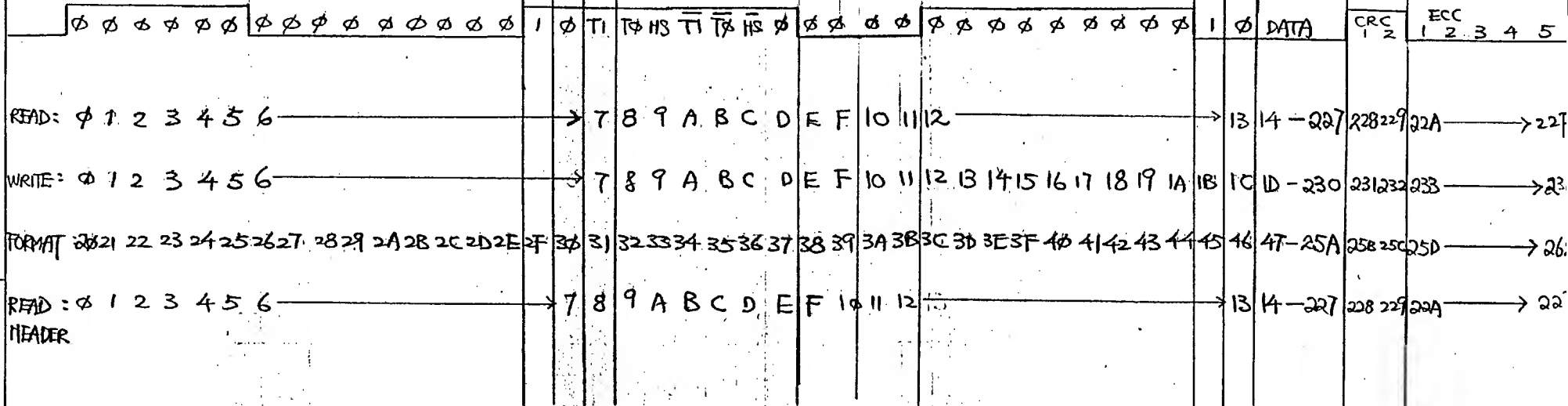
P9.

X=1000

PGT

WTGT

← 532 →



P10

# WIDGET CONTROLLER (19 SECTORS) Z8 PINOUTS..

